

Quartet II Condensed Operating Instructions - Front Panel



DUAL TRANSFORMER MIC/DI PREAMP

FULL/FOCUSED: Selects between mic input transformers with turns ratio of 1:5 (Full) or 1:10 (Focused).

1500/10K: Changes the load impedance seen by the microphone, which can change the tone and overload characteristics of the mic.

DI INPUT: Selects between Instrument (10M at the front 1/4" jack, 1M at the rear jack) and Line (100K with a -20dB pad at both inputs).

MIC/DI: Selects between the MIC input (via the XLR input connector) or DI input signals (via the front or rear 1/4" input jacks).

+48V: 48 volts to the XLR input for mics requiring phantom power.

0/-20: Inserts a 20 dB pad (40dB if DI switch in 'Line' position) in front of the mic input transformer .

+/-: Mic phase reversal switch. Normal phase is XLR pin 2 hot.

LO CUT: An 18dB/octave low frequency rolloff at 75 or 150 Hz.

GAIN: +33 to +63dB (Full/1:5), +40 to +70dB (Focused/1:10), +20dB to + 50dB for the DI instrument input, and 0 to +30dB for the DI line input, in 3dB steps.

OUTPUT: Controls the output level of the preamp stage. By increasing the Gain and lowering the Output level, the sound of the preamp can be varied.

PASSIVE/AGGRESSIVE EQUALIZER

HI Peak: up to +20dB (bandwidth-dependent) from 0.8-18 kHz

MID Dip: up to -20dB (bandwidth-dependent) from 200-800 Hz .

LO Boost/LO Cut: up to 20dB shelving from 20-120 Hz.

EQ - μ / μ - EQ: Places the EQ before or after the compressor.

PASSIVE/AGGRESSIVE: The Aggressive mode increases the saturation of the EQ circuit to add harmonic content.

DELTA-MU COMPRESSOR/LIMITER

THRESHOLD: Up to 15 dB of compression is possible.

OUTPUT: Off to +15 dB of gain make-up after compression.

MODE: There are five modes of compression: Fast, Faster (both peak-averaged), Vintage (program dependent attack and release times), Average (rms detection), or manual (attack 1-100ms, release 0.1-2s).

SIDCHAIN LO CUT: Reduces detector sensitivity below 150 Hz.

PEAK LIMITER

CEILING: Determines the maximum output level for peak limiting.

JFET/MOSFET: Selects the mode of FET peak limiting.

Note: All four elements are independent and may be patched out and used separately.
